

Electrical Networks

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A. Henderson

VSSD

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Preface

The book begins with simple basic concepts and the principal circuit theorems, which form a good link to the knowledge of the starting student; initially the mathematics is not difficult either. Very soon the controlled sources (including opamps) are introduced.

Subsequently the whole theory is extended to alternating currents including complex voltages, currents and impedances, while the mathematics becomes increasingly complicated. The subsequent chapters discuss transformers, three-phase systems, Fourier analysis, the complex frequency, poles and zeros, two ports including filters and networks with switches (transient response). Then an extensive chapter on computer aided design follows; it turns out that the problems dealt with before can be solved by computer.

Each chapter finishes with a large number of problems with increasing difficulty. The book concludes with the answers to the problems.

In the field of network theory one cannot avoid a great influence of mathematics, but in many places I explain the physical background of mathematical results.

In order to avoid calculations that are too complex I have in most cases used simple values for the network elements.

I wish to express my appreciation to Professor K.M. Adams and Professor P. Dewilde for the many informative conversations I had with them and to my colleague W. Buijze for his critical remarks and because he was so kind as to offer me a number of instructive problems.

Finally I wish to thank J. Schievink (VSSD) for his splendid and friendly cooperation and his suggestions.

Delft, December 1989

A. Henderson

Contents

PREFACE	5
CONTENTS	6
SYMBOLS	10
1 D.C. CURRENTS AND D.C. VOLTAGES	13
1.1 Current, potential, voltage and resistance	13
1.2 The voltage source and the current source	15
1.3 Energy and power	17
1.4 Connection of resistors	18
1.5 Voltage and current division	18
1.6 The solution of larger networks	19
1.7 The mesh method	20
1.8 The node method	22
1.9 The current law for a cut-set	24
1.10 Superposition	24
1.11 Tellegen's theorem	26
1.12 Two-ports	28
1.13 Thévenin's and Norton's theorems	30
1.14 Maximum power transfer	32
1.15 Star-delta transformation	33
1.16 Controlled sources	34
1.17 Transactors	34
1.18 Special networks	37
1.19 Thévenin's theorem in networks with transactors	40
1.20 The operational amplifier	42
1.21 Problems	45
2 VARYING CURRENTS AND VOLTAGES	59
2.1 Introduction	59
2.2 Direction and polarity	59
2.3 Kirchhoff's laws and Ohm's law	60
2.4 Periodical quantities	60
2.5 Average value	60
2.6 Power	60
2.7 Effective value	61
2.8 Sinusoidal quantities	62
2.9 Power for sinusoidal voltages and currents	64
2.10 The sum of two sinusoidal quantities	66

2.11	The capacitor	66
2.12	The inductor	67
2.13	Energy stored in capacitor and inductor	68
2.14	The passive elements in sinusoidal excitation	69
2.15	A larger network in sinusoidal excitation	70
2.16	Complex numbers	74
2.17	Operations with complex numbers	75
2.18	Complex voltages and currents	76
2.19	Kirchhoff's laws for complex quantities	83
2.20	The complex \rightarrow time transformation	88
2.21	Vector diagrams	92
2.22	Complex power	96
2.23	Maximum power transfer for complex quantities	100
2.24	Problems	101
3	SOME PROPERTIES OF NETWORKS	115
3.1	Polar diagrams	115
3.2	Bode diagrams	122
3.3	Duality	128
3.4	Resonance	130
3.5	Lossless one-ports	138
3.6	Zobel networks	145
3.7	Problems	147
4	MAGNETIC COUPLED INDUCTORS, TRANSFORMERS	155
4.1	Introduction	155
4.2	Mutual induction	155
4.3	Current direction, voltage polarity and the mode of winding	157
4.4	The value of $ M $	158
4.5	Hopkinson's formula	160
4.6	The transformer	161
4.7	Impedance transformation	164
4.8	Equivalent circuits for magnetic coupled coils	166
4.9	The voltage and the current transformer	168
4.10	Problems	169
5	THREE-PHASE SYSTEMS	175
5.1	The rotating field	175
5.2	The creation of a rotating field	176
5.3	The principle of the three-phase motor	176
5.4	The principle of the three-phase generator	178
5.5	The three-phase supply	178
5.6	Complex three-phase voltages	180
5.7	The three-phase load	180

8 Contents

5.8	Power in three-phase systems	184
5.9	Phase compensation	185
5.10	Problems	188
6	FOURIER SERIES	195
6.1	Introduction	195
6.2	The infinity series of Fourier	196
6.3	The frequency spectrum	198
6.4	Dirichlet's conditions	200
6.5	Gibb's phenomenon	200
6.6	The Fourier series in complex form	203
6.7	The finite Fourier series	205
6.8	Reflections on symmetry	208
6.9	The effective value of a Fourier series	210
6.10	Problems	212
7	THE COMPLEX FREQUENCY	217
7.1	Introduction	217
7.2	A capacitor discharges over a resistor	217
7.3	A capacitor discharges over an inductor with series resistor	219
7.4	The complex frequency plane	223
7.5	Extension of the meaning of impedance	224
7.6	Kirchhoff's laws if the frequency is complex	226
7.7	Poles and zeros	228
7.8	Frequency characteristics	230
7.9	The coincidence of poles and zeros	234
7.10	The order of a network	235
7.11	Natural oscillations of a one-port	236
7.12	The location of the poles and zeros for an immittance	239
7.13	The number of poles for an immittance	240
7.14	Poles and zeros on the imaginary axis	240
7.15	The amplitude surface	241
7.16	Problems	243
8	TWO-PORTS, FILTERS	251
8.1	Introduction	251
8.2	Two-port matrices	251
8.3	Interconnection of two-ports	256
8.4	Reciprocity	260
8.5	Restoring the port condition	262
8.6	Poles and zeros of a transfer function	264
8.7	Once more the operational amplifier (opamp)	265
8.8	The realisation of differential equations	268
8.9	Filters	270

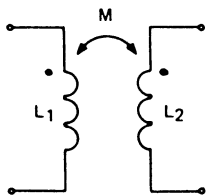
8.10	Problems	274
9	NETWORKS CONTAINING SWITCHES	283
9.1	Introduction	283
9.2	Discontinuity	283
9.3	The continuity theorem	284
9.4	Initial conditions	286
9.5	A second-order transient problem	287
9.6	Natural oscillations	291
9.7	The impulse function	291
9.8	Sine and cosine functions with decreasing amplitude	294
9.9	Summarising example	296
9.10	Conclusion	300
9.11	Problems	301
10	COMPUTER AIDED ANALYSIS	305
10.1	Introduction	305
10.2	The NA-matrix	305
10.3	The concept 'stamp'	306
10.4	The MNA-matrix	306
10.5	Outline of the stamps	307
10.6	Earthing a terminal	315
10.7	Connecting two terminals	316
10.8	A terminal becomes an internal node	317
10.9	Examples	318
10.10	The solution of the matrix equation	318
10.11	Numerical integration	319
10.12	Equivalent circuits	324
10.13	Nonlinear elements	337
10.14	Problems	344
	APPENDICES	353
	Appendix I Linearity and superposition	353
	Appendix II Tellegen's theorem	357
	Appendix III Reciprocity	359
	Appendix IV Thévenin's and Norton's theorems	363
	Appendix V Star-delta transformation	368
	Appendix VI Foster's theorem	371
	ANSWERS TO PROBLEMS	379
	INDEX	413

Symbols

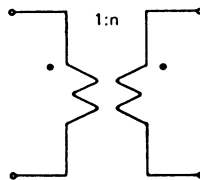
		<i>unit</i>
Admittance matrix	\mathcal{Y}	S
Admittance	Y	S
Ampere turns	(AT)	A
Amplitude and modulus of the voltage	$ V $	V
Amplitude and modulus of the current	$ I $	A
Angular frequency	ω	rad/s
Apparent power	$ S $	VA
Argument of the impedance Z	$\arg Z$	rad
Average, real power	P	W
Bandwidth	B	Hz or rad/s
Capacity	C	F
Cascade matrix	\mathcal{K}	
Coefficient of coupling	k	
Complex frequency	λ	s^{-1}
Complex power	S	VA
Conductance	G	S
Conductance matrix	\mathcal{G}	S
Conjugate complex of Z	Z^*	Ω
Constant charge	Q	C
Constant, or complex current	I	A
Constant, or complex voltage	V	V
Coupled flux	Φ	Vs
Damping exponent	σ	s^{-1}
Determinant of the matrix	$\det Z$ or $ Z $	
Detuning	d	
Differentiating operator	p	s^{-1}
Effective value of the current I	I_{eff}	A
Electric field	E	V/m
Energy	W	J
Force	F	N
Frequency	f	Hz
Hybrid matrix	\mathcal{H}	
Impedance matrix	Z	Ω
Impedance	Z	Ω
Inductor	L	H
Integrating operator	p^{-1}	s
Leak	σ	

Magnetic field	H	A/m
Magnetic fluxdensity	B	Vs/m ²
Magnetic resistance	R _m	A/Vs
Momental charge	q	C
Momental current	i	A
Momental power	p	W
Momental voltage	v	V
Mutual induction	M	H
Period	T	s
Permeability of the vacuum	μ ₀	Vs/Am
Phase	φ or p	rad
Quality of an inductor	Q	
Reactance	X	Ω
Reactive power	Q	VA _r
Relative permeability	μ _r	
Resistance matrix	\mathcal{R}	Ω
Resistance	R	Ω
Reverse cascade matrix	J	
Reverse hybrid matrix	G	
Specific resistance	ρ	Ω/m/mm ²
Specific conductance	γ	S/m/mm ²
Susceptance	B	S
Time constant	τ	s
Voltage or current ratio	H	
Voltage ratio in decibel	G	dB
Voltage	V _{ab} = V _a - V _b	V

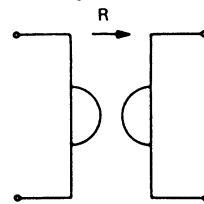
Magnetic coupled inductors



Transformer



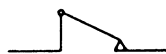
Gyrator



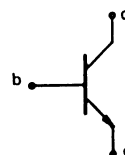
Switch:
make contact



Switch:
break contact

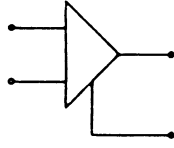


Transistor

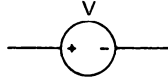


12 Symbols

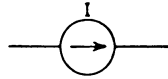
Operational amplifier



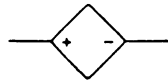
Voltage source



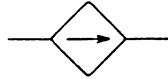
Current source



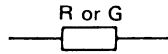
Controlled voltage source



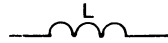
Controlled current source



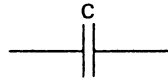
Resistor or conductor



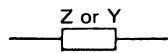
Inductor



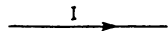
Capacitor



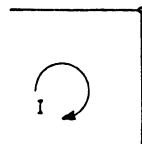
Impedance or admittance



Branch current



Mesh current



Non-linear resistor



Diode



1

d.c. currents and d.c. voltages

1.1 Current, potential, voltage and resistance

The unit of electric *current* is the *ampere*. If a *charge* of 1 *coulomb* (1 C) is passed through the cross-section of a conductor in 1 *second* (1 s), the current will be 1 *ampère* (1 A). The current is indicated by I or i, so if Q is charge and T is time we have

$$I = \frac{Q}{T}. \quad (1.1)$$

We will use capital letters if the quantity is constant and lower case letters if the quantity varies with time. In the latter case for the current we get:

$$i = \frac{dq}{dt}. \quad (1.2)$$

The *potential* V_A (measured in *volt*, V) at a point A is equal in magnitude to the *energy* (measured in *joule*, J) required to take a charge of 1 C from a point where the potential is regarded as zero ($V = 0$) to point A.

The *voltage* V_{AB} between two points A and B is the energy that has to be supplied to take a charge of 1 C from point B to point A. So we have:

$$V_A = V_B + V_{AB},$$

thus

$$V_{AB} = V_A - V_B. \quad (1.3)$$

It follows that

$$V_{BA} = -V_{AB}. \quad (1.4)$$

We assign to each wire in a network a direction and we call this the *positive current direction*. We use this as a reference. One can freely choose this positive current direction. Once chosen, the sign of the current is known.

In Figure 1.1 a wire is drawn with the positive current direction I_1 . It is possible that there is no current in the wire, but we still maintain the arrow with the indication I_1 , I_1 being zero. If there is a current in the wire of 7 A from left to right we write $I_1 = 7$ A; if there is a current in the wire of 5 A from right to left we write $I_1 = -5$ A.



Figure 1.1

In Figure 1.2 a situation is represented in which the current direction changes. Between 0 and t_1 seconds the current i_3 is positive, between t_1 and t_2 the current i_3 is negative.

For the polarity of a voltage we have a similar situation. The arrow of the the positive current direction has the same meaning as the signs + and - in the *positive voltage polarity* (see Figure 1.3.a).

If $V_2 > 0$ the left hand terminal has a higher potential than the right hand terminal. If $V_2 < 0$ the potential of the left hand terminal is smaller than that of the right hand terminal.

As has been already explained we can also denote a voltage between two terminals by two indices. This is beneficial in the case of big networks. The indices give the incident nodes. The first index is the positive terminal of the reference voltage (see Figure 1.3.b).

Thus we can omit the notation + and - and also the notation V_{AB} without difficulties.

Finally, if there is no confusion we can omit the double arrow (see Figure 1.3.c).

The relation between voltage and current in a conductor is given by *Ohm's law*:

$$V = RI, \tag{1.5}$$

in which R is supposed to be linear. R is called *resistance*. In Figure 1.4 it is shown how the positive voltage is related to the positive current.

We see that the positive current flows from plus to minus through the resistor. In other words we can say that *the current and the voltage are interrelated (belong to each other)*.

The resistance of a wire with a constant cross-section is

$$R = \frac{\rho l}{A} = \frac{l}{\gamma A}. \tag{1.6}$$

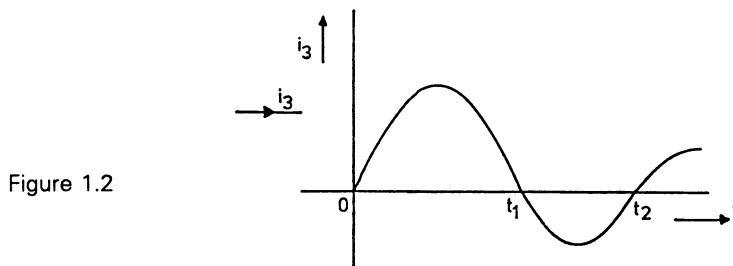


Figure 1.2

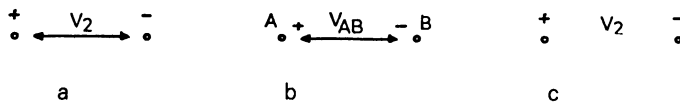


Figure 1.3

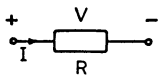


Figure 1.4

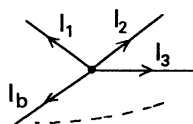


Figure 1.5

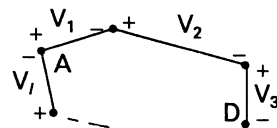


Figure 1.6

In this equation l is the length (m), A is the cross-section in square metres (m^2), ρ is the specific resistance (Ωm) and γ the specific conductance ($(\Omega m)^{-1}$). The values of ρ and γ depend on the material used. ρ and γ also depend on the temperature. This leads to non-linearity which we will discuss at the end of Chapter 10.

The *conductance* G is the inverse of the resistance:

$$G = \frac{1}{R}. \quad (1.7)$$

So Ohm's law can also be written as

$$I = GV. \quad (1.8)$$

We will assume that the connecting wires have no resistance, i.e. $R = 0$.

If $R = 0$ we speak of a *short circuit*; if $G = 0$ we speak of *open terminals*.

Kirchhoff's current law is:

$$\sum_{n=1}^b I_n = 0 \quad n = 1, 2, \dots, b. \quad (1.9)$$

This equation refers to a node, where b wires (*branches*) coincide (see Figure 1.5). This formula also means that the sum of the currents flowing into the node equals the sum flowing out.

Positive currents are those which leave the node. *Kirchhoff's voltage law* is:

$$\sum_{m=1}^l V_m = 0 \quad m = 1, 2, \dots, l. \quad (1.10)$$

This equation refers to a *loop* in which there are l voltages (see Figure 1.6).

Following the loop the drop in voltage is chosen as positive. The use of the node indications with figures or letters, together with the corresponding double voltage index results in a clear formula:

$$V_{AB} + V_{BC} + V_{CD} + V_{DA} = 0.$$

So:

$$V_{AD} = V_{AB} + V_{BC} + V_{CD}. \quad (1.11)$$

Note the position of the indices in the left- and right-hand parts. Equation 1.11 is called the *index rule*. Not all nodes necessarily have a branch between them. The voltage law may also contain voltages of *fictitious* branches.

1.2 The voltage source and the current source

The voltage V_1 between the terminals of an *accumulator* with a varying *load* (see Figure 1.7) and the load current I_b have the idealised plot of Figure 1.8.

The analytic expression for $V_1 = f(I_b)$ is:

$$V_1 = V - R_1 I_b. \quad (1.12)$$

R_i is the *internal resistance*.

With the aid of Kirchhoff's voltage law we can now draw the circuit of Figure 1.9.

I_s is the *short circuit current*. The element V is the (ideal) *voltage source*.

If we divide both terms of equation 1.12 by R_i we get

$$\frac{V_1}{R_i} = \frac{V}{R_i} - I_b \tag{1.13}$$

and together with Kirchhoff's current law this gives the network of Figure 1.10.

We have set $\frac{V}{R_i} = I$ and this is called the (ideal) *current source*.

As a collective name we will use the expression *source intensity* or *source strength* for a voltage source voltage and a current source current.

If we connect a resistor R to a voltage source V the current through that resistor is $I = \frac{V}{R}$ with $R \neq 0$. This leads to the important rule:

A voltage source must not be short-circuited.

If we connect a conductor G to a current source I the voltage across that conductor is $V = \frac{I}{G}$ with $G \neq 0$. This means:

The terminals of a current source must not be opened.

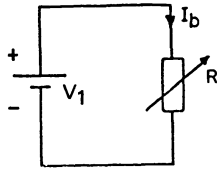


Figure 1.7

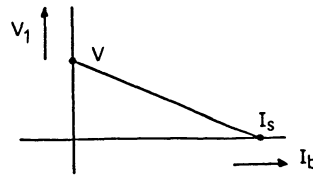


Figure 1.8

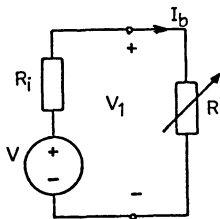


Figure 1.9

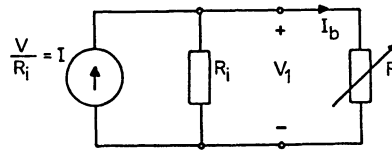


Figure 1.10

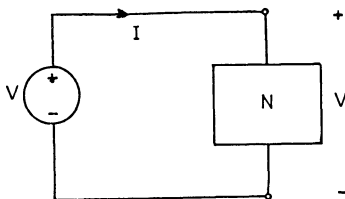


Figure 1.11

A voltage source with intensity zero is a *short circuit* and a current source with intensity zero is *open terminals*.

Two voltage sources in parallel are forbidden if the sum of the source strengths is not zero (for in this case Kirchhoff's voltage law is not valid). Two voltage sources in parallel are not forbidden if the sum of the source strengths is zero. In that case, however, the source currents cannot be calculated.

Two current sources in series are forbidden if the sum of the source strengths is not zero (for in this case Kirchhoff's current law for the node between the two sources) is not valid. Two current sources in series are not forbidden if the sum of the source strengths is zero. In that case the source voltages cannot be calculated.

In network theory it often happens that there is analogy between two formulas, between two elements or between two circuits. For instance, one Kirchhoff's law turns into the other if one substitutes voltage for current and vice versa. We therefore say that the current law is *the dual* of the voltage law and vice versa.

The dual character is also found in

- voltage – current
- open nodes – short circuit
- resistance – conductance

In the following chapters we shall often meet this phenomenon of *duality*.

1.3 Energy and power

The voltage V_{AB} between two points A and B is defined as the work needed to move a unit charge (1 C concentrated in a point) from point B to point A.

If the charge is Δq the work is therefore

$$\Delta W = (V_A - V_B)\Delta q = V_{AB}\Delta q, \quad (1.14)$$

in which V_A and V_B are the potentials of the points A and B. If V_{AB} is constant (d.c.) and if the work is done in a time Δt , the average power is

$$P = \frac{\Delta W}{\Delta t} = V \frac{\Delta q}{\Delta t},$$

in which $V = V_{AB}$.

For $\Delta t \rightarrow 0$ we obtain

$$P = VI. \quad (1.15)$$

So the power, in the case of d.c., is the product of voltage and current.

Energy is expressed in joule (J), power in watt (W).

Power can be consumed or supplied. If a current I flows through a network N with two terminals (also called a *one-port*) and if the polarity of the voltage V is such that I flows from + to – the power consumed is positive (Figure 1.11).

The voltage source transports (positive) charge from minus to plus and so delivers electrical energy to N (this energy is supplied by the chemical or mechanical system outside the network).

The network N consumes this energy. We have pointed out before that the voltage and the current of a two-terminal network *belong to each other* if the current flows from plus to minus through the network. So we derive the following rule:

The energy $P = VI$ consumed is positive if current and voltage belong to each other.

We note that the current I does not necessarily have to be supplied by a voltage source; it can also be a current source or in general any other network.

1.4 Connection of resistors

One can easily show, using both Kirchhoff's laws and Ohm's law, that for the series connection (Figure 1.12) the total resistance is the sum of the separate resistances.

$$R = \sum_{k=1}^n R_k. \quad (1.16)$$

The dual situation is the parallel connection (see Figure 1.13). We find

$$G = \sum_{k=1}^n G_k. \quad (1.17)$$

1.5 Voltage and current division

A formula which is often used is the formula of *voltage division*. Consider the network of Figure 1.14. We calculate V_2 , and find $I = \frac{V}{R_1 + R_2}$ and $V_2 = R_2 I$.

So

$$V_2 = \frac{R_2}{R_1 + R_2} \cdot V. \quad (1.18)$$

This is called *voltage division*.

The dual of voltage division is the *current division* (Figure 1.15). We find

$$I_2 = \frac{G_2}{G_1 + G_2} \cdot I.$$

So

$$I_2 = \frac{G_2}{G_1 + G_2} \cdot I. \quad (1.19)$$

Sometimes this formula is written with resistances. With $G_1 = \frac{1}{R_1}$ and $G_2 = \frac{1}{R_2}$ we obtain

$$I_2 = \frac{R_1}{R_1 + R_2} \cdot I. \quad (1.20)$$

Note the index in the numerator.

1.6 The solution of larger networks

In general the solution of a network problem involves the computing of all voltages and currents in the network. As an example we shall solve the bridge circuit and attach a current to each branch (Figure 1.16).

Kirchhoff's current law for the nodes A, B, C and D gives:

$$I_1 = I_2 + I_5 \tag{a}$$

$$I_2 = I_3 + I_4 \tag{b}$$

$$I_1 = I_3 + I_6 \tag{c}$$

$$I_6 = I_4 + I_5 \tag{d}$$

Not all of these equations are independent. It follows from formulas (a) and (b) and from (c) and (d) that:

$$I_1 = I_3 + I_4 + I_5.$$

Kirchhoff's voltage law for the loops 1, 2 and 3 gives:

$$V_{AD} + V_{DC} + V_{CA} = 0 \tag{e}$$

$$V_{AB} + V_{BD} + V_{DA} = 0 \tag{f}$$

$$V_{BC} + V_{CD} + V_{DB} = 0 \tag{g}$$

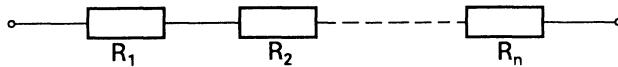


Figure 1.12

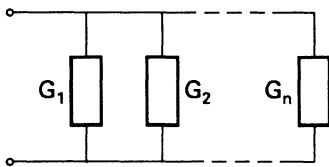


Figure 1.13

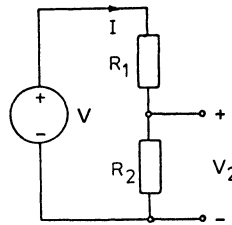


Figure 1.14

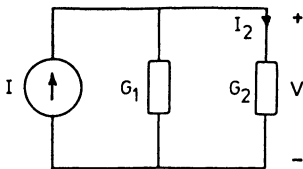


Figure 1.15

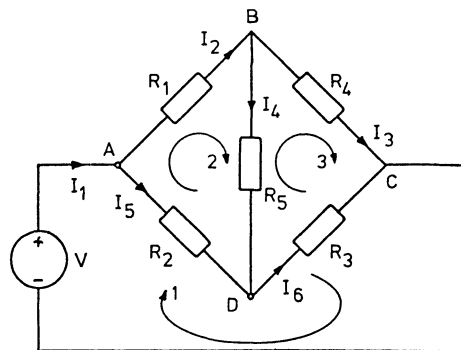


Figure 1.16

These three loops, each not enclosing smaller loops, are called *meshes*. Kirchhoff's voltage law applied to the loop ABCD results in:

$$V_{AB} + V_{BC} + V_{CD} + V_{DA} = 0. \quad (\text{h})$$

Formula (h) is dependent, because adding (f) and (g) yields (h). Finally we apply Ohm's law:

$$V_{AB} = R_1 I_2, \quad (\text{i})$$

$$V_{AD} = R_2 I_5, \quad (\text{j})$$

$$V_{BD} = R_5 I_4, \quad (\text{k})$$

$$V_{BC} = R_4 I_3, \quad (\text{l})$$

$$V_{DC} = R_3 I_6. \quad (\text{m})$$

The number of equations we obtain with this so-called *branch method* is not convenient and is also unnecessarily large. We shall now discuss two more systematic methods.

1.7 The mesh method

We again examine the bridge circuit and attach a so-called *mesh current* to each mesh. This is a fictitious current which *circulates* in a mesh (see Figure 1.17).

Consequently a *branch current* is the difference between two mesh currents if that branch is the separation of two meshes:

$$I_4 = I_2 - I_3. \quad (1.21)$$

In an outer mesh the mesh current is equal to the branch current, e.g. I_1 in the source and I_2 in R_1 . Now apply the voltage law to the three meshes. We write the intensity of the source in the left-hand part of the equation and the voltages across the resistors in the right-hand part:

$$V = R_2(I_1 - I_2) + R_3(I_1 - I_3)$$

or, in another sequence

$$V = (R_2 + R_3)I_1 - R_2I_2 - R_3I_3. \quad (\alpha)$$

The first term in the right-hand part is the voltage created by the mesh current in the sum of the resistances in the mesh, the other terms are negative and are created by the 'counteracting' currents in the adjacent meshes.

Further note that the source voltage is positive, if, following the mesh direction, the plus terminal is left. In a similar way for the other meshes we find:

$$0 = -R_2I_1 + (R_1 + R_5 + R_2)I_2 - R_5I_3, \quad (\beta)$$

$$0 = -R_3I_1 - R_5I_2 + (R_4 + R_3 + R_5)I_3. \quad (\gamma)$$

With the three equations (α), (β) and (γ) we can solve the three mesh currents. All branch currents (and thus all branch voltages) are then known. In matrix notation the three mesh equations become: